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Registered [1] Optimal printing method for solder mask screen (인쇄회로기판 제조의 Solder Mask Screen 인쇄 방법) PUB E-



Application No. (Date) : 1020130167442 (2013, 12, 30)
 IPC : H05K 3/34
 Applicant : DAP CORP.
 Right holder (current) : DAP CORP.

KPA (Korea Patent Abstract) The present invention relates to a method to manufacture a printed circuit board (PCB) and, more specifically, to the development of a method to prevent a short circuit in the progress of surface mount technology (SMT) by equally applying a solder mask to a surface of a final layer. A method to optimize the solder mask screen printing of PCB manufacture includes a screen panel manufacturing step of placing a screen for printing in a frame to manufacture a screen panel; a screen panel setting step of cleaning the screen panel and then setting the screen panel on

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Apparatus for attaching glass and mask

기판과 마스크를 결합하는 장치

Citation information will be seen

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Details Biographical Information Legal Status Claim Designated States **Citation** Family Patent

※ The information is based on the citation information attached to a Notification of reason for refusal by the examiner.

▶ Forward Citation

Country	Pub. Date	Pub. No	Title	IPC
Republic of Korea	1020060110805 A	2006, 10, 25	Magnetic Mask Holder	H05B 33/10
Republic of Korea	1020080045886 A	2008, 05, 26	BIG-SIZE HYBRID OPEN MASK AND MANUFACTURING IS PACKAGED AND MANUFACTURING METHOD OF ORGANIC ELECTRO-LUMINESCENCE DISPLAY THE SAME	H05B 33/10

▶ Backward Citation

Application No	Application Date	Title	IPC
1020180050432	2018, 05, 02	DEPOSITION SYSTEM	C23C 14/04